



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/797,296  
Applicant : Dana LEE  
Filed : March 9, 2004  
TC/A.U. : 2822  
Examiner : Kiesha L. ROSE  
Title : BURIED BIT LINE NON-VOLATILE FLOATING GATE  
MEMORY CELL WITH INDEPENDENT CONTROLLABLE  
CONTROL GATES IN A TRENCH, AND ARRAY  
THEREFORE, AND METHOD OF FORMATION  
  
Docket No. : 351913-992471 (Formerly 2102397-992471)  
Customer No. : 26379

CERTIFICATE OF MAILING

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DLA PIPER RUDNICK GRAY CARY Date: June 1, 2006

By: Rosa A. Caviedes  
Rosa A. Caviedes

**RESPONSE TO OFFICE ACTION OF FEBRUARY 10, 2006**

Sir:

In response to the Office Action of February 10, 2006, Applicants hereby submit the following remarks and arguments traversing the rejection of the pending claims.

**Remarks/Arguments** begin on page 2 of this paper.